

IP Name:	Digital Error-Averaging Technique for Pipelined ADC
Grade:	Verification
Process:	0.5um
Foundry:	TSMC
Features:	5V ENOB: 8.4 bits
Release Notes:	Sampling rate 5 MHz SNDR=52.5447dB DNL ± 0.35 LSB INL ± 0.55 LSB
Supply Voltage	5 V
Operating Frequency	5 MHz
Power Consumption	300 mW
Die Size	16.932 mm ²
Aspect Ratio	0.683

