

IP Name:	A spread spectrum clock generator based on a sigma -delta PLL
Grade:	Verification
Process:	0.35um
Foundry:	TSMC
Features:	VCO gain 153MH/V charge pump current 250uA Loop bandwidth 500KHz
Release Notes:	In band phase noise -60dBc/Hz frequency resolution < 7KHz Reference Spurs < -100dBc/Hz Fraction Spurs < -70dBc/Hz
Supply Voltage	3.3V
Operating Frequency	2.75GHz
Power Consumption	160 mW
Die Size	2.3896mm <sup>2</sup>
Aspect Ratio	0.56

