

IP Name:	CMOS DLL and Transmitter
Grade:	Verification
Process:	0.25um
Foundry:	TSMC
Features:	2.5V 50~600MHz 400mW @ 250MHz
Release Notes:	Jitter @ 50MHz 9.983ps(RMS) 72ps(PkPk) Jitter @ 250MHz 4.153ps(RMS) 30.8ps(PkPk) Jitter @ 600MHz 1.796ps(RMS) 16ps(PkPk)
Supply Voltage (Min)	2.5 V
Operating Frequency (Max)	600 MHz
Output Frequency (Min)	50MHz
Output Frequency (Max)	600MHz
Power Consumption (Min)	400 mW
Operating Frequency (Min)	50MHz
Clock Jitter (Min)	1.796ps
Clock Jitter (Max)	9.983ps
Die Size	3.366 mm <sup>2</sup>
Aspect Ratio	0.86

