

IP Name:	CMOS Low-Noise Amplifier (LNA) Design
Grade:	Verification
Process:	0.18um
Foundry:	TSMC
Features:	1V IIP3 -5.2dBm NF 5.2dB @ 5.2-GHz
Release Notes:	Center Frequency 5.2 GHz S21 7.4dB @ 5.2-GHz S11 -10.9dB @ 5.2-GHz S22 -11.2dB @ 5.2-GHz
Supply Voltage	1 V
Power Consumption	16 mW
Die Size	0.978 mm <sup>2</sup>
Aspect Ratio	0.846

